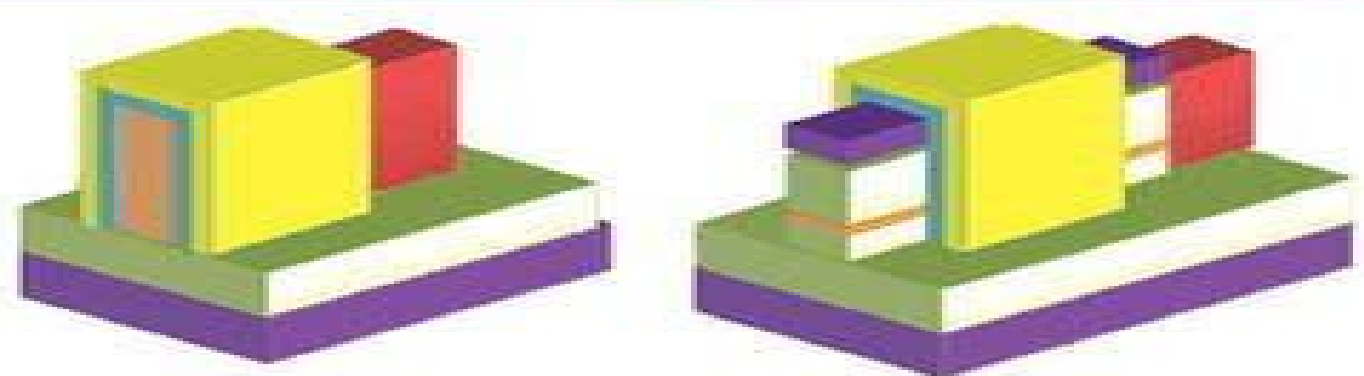


Nanoscale Silicon Devices

EDITED BY **Shunri Oda** • **David K. Ferry**



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Nanoscale Silicon Devices Shunri Oda, David K. Ferry, 2018-09-03 Is Bigger Always Better Explore the Behavior of Very Small Devices as Described by Quantum Mechanics Smaller is better when it comes to the semiconductor transistor Nanoscale Silicon Devices examines the growth of semiconductor device miniaturization and related advances in material device circuit and system design and highlights the use of device scaling within the semiconductor industry Device scaling the practice of continuously scaling down the size of metal oxide semiconductor field effect transistors MOSFETs has significantly improved the performance of small computers mobile phones and similar devices The practice has resulted in smaller delay time and higher device density in a chip without an increase in power consumption This book covers recent advancements and considers the future prospects of nanoscale silicon Si devices It provides an introduction to new concepts including variability in scaled MOSFETs thermal effects spintronics based nonvolatile computing systems spin based qubits magnetoelectric devices NEMS devices tunnel FETs dopant engineering and single electron transfer new materials such as high k dielectrics and germanium and new device structures in three dimensions It covers the fundamentals of such devices describes the physics and modeling of these devices and advocates further device scaling and minimization of energy consumption in future large scale integrated circuits VLSI Additional coverage includes Physics of nm scaled devices in terms of quantum mechanics Advanced 3D transistors tri gate structure and thermal effects Variability in scaled MOSFET Spintronics on Si platform NEMS devices for switching memory and sensor applications The concept of ballistic transport The present status of the transistor variability and more An indispensable resource Nanoscale Silicon Devices serves device engineers and academic researchers including graduate students in the fields of electron devices solid state physics and nanotechnology

Silicon Materials V. Alexander Stefan (Editor-in-Chief), 2002 *Silicon Nanoelectronics* Shunri Oda, David Ferry, 2017-12-19 Technological advancement in chip development primarily based on the downscaling of the feature size of transistors is threatening to come to a standstill as we approach the limits of conventional scaling For example when the number of electrons in a device's active region is reduced to less than ten electrons or holes quantum fluctuation errors will occur and when gate insulator thickness becomes too insignificant to block quantum mechanical tunneling unacceptable leakage will occur Fortunately there is truth in the old adage that whenever a door closes a window opens somewhere else In this case that window opening is nanotechnology Silicon Nanoelectronics takes a look at at the recent development of novel devices and materials that hold great promise for the creation of still smaller and more powerful chips Silicon nanodevices are positioned to be particularly relevant in consideration of the existing silicon process infrastructure already in place throughout the semiconductor industry and silicon's consequent compatibility with current CMOS circuits This is reinforced by the nearly perfect interface that can exist between natural oxide and silicon Presenting the contributions of more than 20 leading academic and corporate researchers from the United States and Japan Silicon

Nanoelectronics offers a comprehensive look at this emergent technology. The text includes extensive background information on the physics of silicon nanodevices and practical CMOS scaling. It considers such issues as quantum effects and ballistic transport and resonant tunneling in silicon nanotechnology. A significant amount of attention is given to the all important silicon single electron transistors and the devices that utilize them. In offering an update of the current state of the art in the field of silicon nanoelectronics, this volume serves well as a concise reference for students, scientists, engineers, and specialists in various fields. *APPC 2000, Procs Of The 8th Asia-pacific Physics Conference* Chia-seng Chang, Shang Fan Lee, Yeong Der Yao, Hai Yang Cheng, 2001-05-24. This volume in contemporary physics records the blossoming physical activities that have occurred at the turn of the millennium including the most up to date and exciting scientific and technological discoveries of recent years. The book can serve as a guide or quick reference for professionals in related fields.

APPC 2000 Yeong-Der Yao, 2001. This volume in contemporary physics records the blossoming physical activities that have occurred at the turn of the millennium including the most up to date and exciting scientific and technological discoveries of recent years. The book can serve as a guide or quick reference for professionals in related fields. **CERN Courier**, 2016. *Dielectrics for Nanosystems II* D. Misra, H. Iwai, 2006. This issue covers papers relating to advanced semiconductor products that are true representatives of nanoelectronics and that have reached below 100nm. Depending on the application, the nanosystem may consist of one or more of the following types of functional components: electronic, optical, magnetic, mechanical, biological, chemical, energy source, and various types of sensing devices. As long as one or more of these functional devices is in the 1-100nm dimensions, the resultant system can be defined as a nanosystem. Papers will be in all areas of dielectric issues in nanosystems. In addition to traditional areas of semiconductor processing and packaging of nanoelectronics, emphasis will be placed on areas where multifunctional device integration through innovation in design, materials, and processing at the device and system levels will lead to new applications of nanosystems. **Quantum**

Confined Semiconductor Nanostructures: Volume 737 Victor I. Klimov, 2003-04-16. The MRS Symposium Proceeding series is an internationally recognised reference suitable for researchers and practitioners. This book brings together a single comprehensive overview of recent progress and future directions in nanoscale semiconductor research. Fields ranging from materials science to physics, chemistry, electrical and microelectronic engineering, circuit design, and more are represented.

Japanese Journal of Applied Physics, 2008. *JJAP*, 2010. *AIST Today*, 2001. *JJAP Letters*, 2005. **Journal of the Physical Society of Japan**, 2003. *Microcrystalline and Nanocrystalline Semiconductors*, 2001. [Silicon Nanoelectronics](#) Shunri Oda, David Ferry, 2017-12-19. Technological advancement in chip development primarily based on the downscaling of the feature size of transistors is threatening to come to a standstill as we approach the limits of conventional scaling. For example, when the number of electrons in a device's active region is reduced to less than ten electrons or holes, quantum fluctuation errors will occur, and when gate insulator thickness becomes too insignificant to block quantum

mechanical tunneling unacceptable leakage will occur Fortunately there is truth in the old adage that whenever a door closes a window opens somewhere else In this case that window opening is nanotechnology Silicon Nanoelectronics takes a look at at the recent development of novel devices and materials that hold great promise for the creation of still smaller and more powerful chips Silicon nanodevices are positioned to be particularly relevant in consideration of the existing silicon process infrastructure already in place throughout the semiconductor industry and silicon s consequent compatibility with current CMOS circuits This is reinforced by the nearly perfect interface that can exist between natural oxide and silicon Presenting the contributions of more than 20 leading academic and corporate researchers from the United States and Japan Silicon Nanoelectronics offers a comprehensive look at this emergent technology The text includes extensive background information on the physics of silicon nanodevices and practical CMOS scaling It considers such issues as quantum effects and ballistic transport and resonant tunneling in silicon nanotechnology A significant amount of attention is given to the all important silicon single electron transistors and the devices that utilize them In offering an update of the current state of the art in the field of silicon nanoelectronics this volume serves well as a concise reference for students scientists engineers and specialists in various fields in

Physics,2001 **Simulation of Nanoscale Silicon Devices with Combined Monte Carlo/quantum Approaches** Ranganathan Ravishankar,2006 **Stress and Strain Engineering at Nanoscale in Semiconductor Devices** Chinmay K. Maiti,2021-06-29 Anticipating a limit to the continuous miniaturization More Moore intense research efforts are being made to co integrate various functionalities More than Moore in a single chip Currently strain engineering is the main technique used to enhance the performance of advanced semiconductor devices Written from an engineering applications standpoint this book encompasses broad areas of semiconductor devices involving the design simulation and analysis of Si heterostructure silicongeranium SiGe and III N compound semiconductor devices The book provides the background and physical insight needed to understand the new and future developments in the technology CAD TCAD design at the nanoscale Features Covers stressstrain engineering in semiconductor devices such as FinFETs and III V Nitride based devices Includes comprehensive mobility model for strained substrates in global and local strain techniques and their implementation in device simulations Explains the development of strain stress relationships and their effects on the band structures of strained substrates Uses design of experiments to find the optimum process conditions Illustrates the use of TCAD for modeling strain engineered FinFETs for DC and AC performance predictions This book is for graduate students and researchers studying solid state devices and materials microelectronics systems and controls power electronics nanomaterials and electronic materials and devices

Integrated Silicon-Metal Systems at the Nanoscale Munir H. Nayfeh,Ammar Nayfeh,2023-04-12 *Integrated Silicon Metal Systems at the Nanoscale Applications in Photonics Quantum Computing Networking and Internet* is a comprehensive guide to the interaction materials and functional integration at the nanoscale of the silicon metal binary system and a variety of emerging and next generation advanced device applications

from energy and electronics to sensing quantum computing and quantum internet networks The book guides the readers through advanced techniques and etching processes combining underlying principles materials science design and operation of metal Si nanodevices Each chapter focuses on a specific use of integrated metal silicon nanostructures including storage and resistive next generation nano memory and transistors photo and molecular sensing harvest and storage device electrodes phosphor light converters and hydrogen fuel cells as well as future application areas such as spin transistors quantum computing hybrid quantum devices and quantum engineering networking and internet Provides detailed coverage of materials design and operation of metal Si nanodevices Offers a step by step approach supported by principles methods illustrations and equations Explores a range of cutting edge emerging applications across electronics sensing and quantum computing

Nanoscale Devices Gianfranco Cerofolini, 2009-08-26 The second half of the twentieth century and the beginning of the twenty first have been characterized by the most impressive industrial revolution ever seen In approximately 40 years the complexity of integrated circuits ICs has increased by a factor of 10 with a corresponding reduction of the cost per bit by eight orders of magnitude Not only has this evolution allowed dramatic progress in scientific fields large computers space probes etc but also has fueled the economic development with the rise of new markets personal computers cellular phones etc and even social revolutions world wide web global village etc In last years however the situation has significantly changed the continuous scaling down of device size has eventually brought the IC major technique photolithography to its limits Overcoming its original limits has been proved to be possible but the price to pay for that has changed the playing rules while at the beginning of the IC history the evolution was driven by technology now it is driven by economy the cost of a medium size production plant being in the range of a few billion dollars

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