Andreas Hansson
Kees Goossens

On-Chip Interconnect with aelite

Composable and Predictable Systems

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On Chip Interconnect With Aelite Composable And Predictable Systems Embedded Systems

Benny Akesson,Kees Goossens

On Chip Interconnect With Aelite Composable And Predictable Systems Embedded Systems:

   On-Chip Interconnect with aelite Andreas Hansson,Kees Goossens,2010-10-20 The book provides a comprehensive description and implementation methodology for the Philips NXP Aethereal aelite Network on Chip NoC The presentation offers a systems perspective starting from the system requirements and deriving and describing the resulting hardware architectures embedded software and accompanying design flow Readers get an in depth view of the interconnect requirements not centered only on performance and scalability but also the multi faceted application driven requirements in particular composability and predictability The book shows how these qualitative requirements are implemented in a state of the art on chip interconnect and presents the realistic quantitative costs   **Memory Controllers for Real-Time Embedded Systems** Benny Akesson,Kees Goossens,2011-09-15 Verification of real time requirements in systems on chip becomes more complex as more applications are integrated Predictable and composable systems can manage the increasing complexity using formal verification and simulation This book explains the concepts of predictability and composability and shows how to apply them to the design and analysis of a memory controller which is a key component in any real time system

   Multiprocessor System-on-Chip Michael Hübner,Jürgen Becker,2010-11-25 The purpose of this book is to evaluate strategies for future system design in multiprocessor system on chip MPSoC architectures Both hardware design and integration of new development tools will be discussed Novel trends in MPSoC design combined with reconfigurable architectures are a main topic of concern The main emphasis is on architectures design flow tool development applications and system design   Bio-Inspired Fault-Tolerant Algorithms for Network-on-Chip Muhammad Athar Javed Sethi,2020-03-17 Network on Chip NoC addresses the communication requirement of different nodes on System on Chip The bio inspired algorithms improve the bandwidth utilization maximize the throughput and reduce the end to end latency and inter flit arrival time This book exclusively presents in depth information regarding bio inspired algorithms solving real world problems focussing on fault tolerant algorithms inspired by the biological brain and implemented on NoC It further documents the bio inspired algorithms in general and more specifically in the design of NoC It gives an exhaustive review and analysis of the NoC architectures developed during the last decade according to various parameters Key Features Covers bio inspired solutions pertaining to Network on Chip NoC design solving real world examples Includes bio inspired NoC fault tolerant algorithms with detail coding examples Lists fault tolerant algorithms with detailed examples Reviews basic concepts of NoC Discusses NoC architectures developed to date   *Predictable and Runtime-Adaptable Network-On-Chip for Mixed-critical Real-time Systems* Sebastian Tobuschat,2019-03-07 The industry of safety critical and dependable embedded systems calls for even cheaper high performance platforms that allow flexibility and an efficient verification of safety and real time requirements In this sense flexibility denotes the ability to online adapt a system to changes e g changing environment application dynamics errors and the reuse ability for different use cases To cope with the increasing complexity of

interconnected functions and to reduce the cost and power consumption of the system multicore systems are used to efficiently integrate different processing units in the same chip Networks on chip NoCs as a modular interconnect are used as a promising solution for such multiprocessor systems on chip MPSoCs due to their scalability and performance Hence future NoC designs must face the aforementioned challenges For safety critical systems a major goal is the avoidance of hazards For this safety critical systems are qualified or even certified to prove the correctness of the functioning under all possible cases A predictable behavior of the NoC can help to ease the qualification process e g formal analysis of the system To achieve the required predictability designers have two classes of solutions isolation quality of service QoS mechanisms and formal analysis For mixed criticality systems isolation and analysis approaches must be combined to efficiently achieve the desired predictability Isolation techniques are used to bound interference between different application classes And analysis can then be applied verifying the real time applications and sufficient isolation properties Traditional NoC analysis and architecture concepts tackle only a subpart of the challenges they focus on either performance or predictability Existing predictable NoCs are deemed too expensive and inflexible to host a variety of applications with opposing constraints And state of the art analyses neglect certain platform pro *System-on-Chip for Real-Time Applications* Wael Badawy,Graham A. Julien,2002-10-31 System on Chip for Real Time Applications will be of interest to engineers both in industry and academia working in the area of SoC VLSI design and application It will also be useful to graduate and undergraduate students in electrical and computer engineering and computer science A selected set of papers from the 2nd International Workshop on Real Time Applications were used to form the basis of this book It is organized into the following chapters Introduction Design Reuse Modeling Architecture Design Techniques Memory Circuits Low Power Interconnect and Technology MEMS System on Chip for Real Time Applications contains many signal processing applications and will be of particular interest to those working in that community Integrated Optical Interconnect Architectures for Embedded Systems Ian O'Connor,Gabriela Nicolescu,2012-11-07 This book provides a broad overview of current research in optical interconnect technologies and architectures Introductory chapters on high performance computing and the associated issues in conventional interconnect architectures and on the fundamental building blocks for integrated optical interconnect provide the foundations for the bulk of the book which brings together leading experts in the field of optical interconnect architectures for data communication Particular emphasis is given to the ways in which the photonic components are assembled into architectures to address the needs of data intensive on chip communication and to the performance evaluation of such architectures for specific applications **On-Chip Communication Architectures** Sudeep Pasricha,Nikil Dutt,2010-07-28 Over the past decade system on chip SoC designs have evolved to address the ever increasing complexity of applications fueled by the era of digital convergence Improvements in process technology have effectively shrunk board level components so they can be integrated on a single chip New on chip communication architectures have

been designed to support all inter component communication in a SoC design These communication architecture fabrics have a critical impact on the power consumption performance cost and design cycle time of modern SoC designs As application complexity strains the communication backbone of SoC designs academic and industrial R D efforts and dollars are increasingly focused on communication architecture design On Chip Communication Architecures is a comprehensive reference on concepts research and trends in on chip communication architecture design It will provide readers with a comprehensive survey not available elsewhere of all current standards for on chip communication architectures A definitive guide to on chip communication architectures explaining key concepts surveying research efforts and predicting future trends Detailed analysis of all popular standards for on chip communication architectures Comprehensive survey of all research on communication architectures covering a wide range of topics relevant to this area spanning the past several years and up to date with the most current research efforts Future trends that with have a significant impact on research and design of communication architectures over the next several years **Dependable Embedded Systems** Jörg Henkel,Nikil Dutt,2020-12-09 This Open Access book introduces readers to many new techniques for enhancing and optimizing reliability in embedded systems which have emerged particularly within the last five years This book introduces the most prominent reliability concerns from today s points of view and roughly recapitulates the progress in the community so far Unlike other books that focus on a single abstraction level such circuit level or system level alone the focus of this book is to deal with the different reliability challenges across different levels starting from the physical level all the way to the system level cross layer approaches The book aims at demonstrating how new hardware software co design solution can be proposed to ef fectively mitigate reliability degradation such as transistor aging processor variation temperature effects soft errors etc Provides readers with latest insights into novel cross layer methods and models with respect to dependability of embedded systems Describes cross layer approaches that can leverage reliability through techniques that are pro actively designed with respect to techniques at other layers Explains run time adaptation and concepts means of self organization in order to achieve error resiliency in complex future many core systems **Advanced Multicore Systems-On-Chip** Abderazek Ben Abdallah,2017-09-10 From basic architecture interconnection and parallelization to power optimization this book provides a comprehensive description of emerging multicore systems on chip MCSoCs hardware and software design Highlighting both fundamentals and advanced software and hardware design it can serve as a primary textbook for advanced courses in MCSoCs design and embedded systems The first three chapters introduce MCSoCs architectures present design challenges and conventional design methods and describe in detail the main building blocks of MCSoCs Chapters 4 5 and 6 discuss fundamental and advanced on chip interconnection network technologies for multi and many core SoCs enabling readers to understand the microarchitectures for on chip routers and network interfaces that are essential in the context of latency area and power constraints With the rise of multicore and many core systems concurrency is becoming a major issue in the daily

life of a programmer Thus compiler and software development tools are critical in helping programmers create high performance software Programmers should make sure that their parallelized program codes will not cause race condition memory access deadlocks or other faults that may crash their entire systems As such Chapter 7 describes a novel parallelizing compiler design for high performance computing Chapter 8 provides a detailed investigation of power reduction techniques for MCSoCs at component and network levels It discusses energy conservation in general hardware design and also in embedded multicore system components such as CPUs disks displays and memories Lastly Chapter 9 presents a real embedded MCSoCs system design targeted for health monitoring in the elderly   *RapidIO* Sam Fuller,2005-07-08 RapidIO The Embedded System Interconnect brings together one essential volume on RapidIO interconnect technology providing a major reference work for the evaluation and understanding of RapidIO Covering essential aspects of the specification it also answers most usage questions from both hardware and software engineers It will also serve as a companion text to the specifications when developing or working with the RapidIO interconnect technology Including the history of RapidIO and case of studies of RapidIO deployment this really is the definitive reference guide for this new area of technology

Embedded System Design: Topics, Techniques and Trends Achim Rettberg,Mauro Zanella,Rainer Domer,Andreas Gerstlauer,Franz Rammig,2007-06-01 This volume presents the technical program of the 2007 International Embedded Systems Symposium held in Irvine California It covers timely topics techniques and trends in embedded system design including design methodology networks on chip distributed and networked systems and system verification It places emphasis on automotive and medical applications and includes case studies and special aspects in embedded system design

Embedded Systems Handbook Richard Zurawski,2005-08-16 Embedded systems are nearly ubiquitous and books on individual topics or components of embedded systems are equally abundant Unfortunately for those designers who thirst for knowledge of the big picture of embedded systems there is not a drop to drink Until now The Embedded Systems Handbook is an oasis of information offering a mix of basic a   **High-Performance and Time-Predictable Embedded Computing** Pinho, Luis Miguel,Quinones, Eduardo,Marongiu, Andrea,2018-07-04 Nowadays the prevalence of computing systems in our lives is so ubiquitous that we live in a cyber physical world dominated by computer systems from pacemakers to cars and airplanes These systems demand for more computational performance to process large amounts of data from multiple data sources with guaranteed processing times Actuating outside of the required timing bounds may cause the failure of the system being vital for systems like planes cars business monitoring e trading etc High Performance and Time Predictable Embedded Computing presents recent advances in software architecture and tools to support such complex systems enabling the design of embedded computing devices which are able to deliver high performance whilst guaranteeing the application required timing bounds Technical topics discussed in the book include Parallel embedded platformsProgramming modelsMapping and scheduling of parallel computationsTiming and schedulability analysisRuntimes and operating systems

The work reflected in this book was done in the scope of the European project P SOCRATES funded under the FP7 framework program of the European Commission High performance and time predictable embedded computing is ideal for personnel in computer communication embedded industries as well as academic staff and master research students in computer science embedded systems cyber physical systems and internet of things **Adaptable Embedded Systems** Antonio Carlos Schneider Beck,Carlos Arthur Lang Lisbôa,Luigi Carro,2012-11-27 As embedded systems become more complex designers face a number of challenges at different levels they need to boost performance while keeping energy consumption as low as possible they need to reuse existent software code and at the same time they need to take advantage of the extra logic available in the chip represented by multiple processors working together This book describes several strategies to achieve such different and interrelated goals by the use of adaptability Coverage includes reconfigurable systems dynamic optimization techniques such as binary translation and trace reuse new memory architectures including homogeneous and heterogeneous multiprocessor systems communication issues and NOCs fault tolerance against fabrication defects and soft errors and finally how one can combine several of these techniques together to achieve higher levels of performance and adaptability The discussion also includes how to employ specialized software to improve this new adaptive system and how this new kind of software must be designed and programmed **Embedded Systems Design with Platform FPGAs** Ronald Sass,Andrew G. Schmidt,2010-09-10 Embedded Systems Design with Platform FPGAs introduces professional engineers and students alike to system development using Platform FPGAs The focus is on embedded systems but it also serves as a general guide to building custom computing systems The text describes the fundamental technology in terms of hardware software and a set of principles to guide the development of Platform FPGA systems The goal is to show how to systematically and creatively apply these principles to the construction of application specific embedded system architectures There is a strong focus on using free and open source software to increase productivity Each chapter is organized into two parts The white pages describe concepts principles and general knowledge The gray pages provide a technical rendition of the main issues of the chapter and show the concepts applied in practice This includes step by step details for a specific development board and tool chain so that the reader can carry out the same steps on their own Rather than try to demonstrate the concepts on a broad set of tools and boards the text uses a single set of tools Xilinx Platform Studio Linux and GNU throughout and uses a single developer board Xilinx ML 510 for the examples Explains how to use the Platform FPGA to meet complex design requirements and improve product performance Presents both fundamental concepts together with pragmatic step by step instructions for building a system on a Platform FPGA Includes detailed case studies extended real world examples and lab exercises *High Performance Embedded Computing* Luis Miguel Pinho,Eduardo Quinones,Marko Bertogna,2022-09-01 Nowadays the prevalence of computing systems in our lives is so ubiquitous that we live in a cyber physical world dominated by computer systems from pacemakers to cars and airplanes These systems demand

for more computational performance to process large amounts of data from multiple data sources with guaranteed processing times Actuating outside of the required timing bounds may cause the failure of the system being vital for systems like planes cars business monitoring e trading etc High Performance and Time Predictable Embedded Computing presents recent advances in software architecture and tools to support such complex systems enabling the design of embedded computing devices which are able to deliver high performance whilst guaranteeing the application required timing bounds Technical topics discussed in the book include Parallel embedded platforms Programming models Mapping and scheduling of parallel computations Timing and schedulability analysis Runtimes and operating systemsThe work reflected in this book was done in the scope of the European project P SOCRATES funded under the FP7 framework program of the European Commission High performance and time predictable embedded computing is ideal for personnel in computer communication embedded industries as well as academic staff and master research students in computer science embedded systems cyber physical systems and internet of things **Embedded Systems Development** Alberto Sangiovanni-Vincentelli,Haibo Zeng,Marco Di Natale,Peter Marwedel,2013-07-19 This book offers readers broad coverage of techniques to model verify and validate the behavior and performance of complex distributed embedded systems The authors attempt to bridge the gap between the three disciplines of model based design real time analysis and model driven development for a better understanding of the ways in which new development flows can be constructed going from system level modeling to the correct and predictable generation of a distributed implementation leveraging current and future research results

Multi-Core Embedded Systems Georgios Kornaros,2018-10-08 Details a real world product that applies a cutting edge multi core architecture Increasingly demanding modern applications such as those used in telecommunications networking and real time processing of audio video and multimedia streams require multiple processors to achieve computational performance at the rate of a few giga operations per second This necessity for speed and manageable power consumption makes it likely that the next generation of embedded processing systems will include hundreds of cores while being increasingly programmable blending processors and configurable hardware in a power efficient manner Multi Core Embedded Systems presents a variety of perspectives that elucidate the technical challenges associated with such increased integration of homogeneous processors and heterogeneous multiple cores It offers an analysis that industry engineers and professionals will need to understand the physical details of both software and hardware in embedded architectures as well as their limitations and potential for future growth Discusses the available programming models spread across different abstraction levels The book begins with an overview of the evolution of multiprocessor architectures for embedded applications and discusses techniques for autonomous power management of system level parameters It addresses the use of existing open source and free tools originating from several application domains such as traffic modeling graph theory parallel computing and network simulation In addition the authors cover other important topics associated with multi core

embedded systems such as Architectures and interconnects Embedded design methodologies Mapping of applications

   Communication Architectures for Systems-on-Chip José L. Ayala,2018-09-03 A presentation of state of the art approaches from an industrial applications perspective Communication Architectures for Systems on Chip shows professionals researchers and students how to attack the problem of data communication in the manufacture of SoC architectures With its lucid illustration of current trends and research improving the performance quality and reliability of transactions this is an essential reference for anyone dealing with communication mechanisms for embedded systems systems on chip and multiprocessor architectures or trying to overcome existing limitations Exploring architectures currently implemented in manufactured SoCs and those being proposed this book analyzes a wide range of applications including Well established communication buses Less common networks on chip Modern technologies that include the use of carbon nanotubes CNTs Optical links used to speed up data transfer and boost both security and quality of service QoS The book s contributors pay special attention to newer problems including how to protect transactions of critical on chip information personal data security keys etc from an external attack They examine mechanisms revise communication protocols involved and analyze overall impact on system performance

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In a fast-paced digital era where connections and knowledge intertwine, the enigmatic realm of language reveals its inherent magic. Its capacity to stir emotions, ignite contemplation, and catalyze profound transformations is nothing lacking extraordinary. Within the captivating pages of **On Chip Interconnect With Aelite Composable And Predictable Systems Embedded Systems** a literary masterpiece penned by a renowned author, readers embark on a transformative journey, unlocking the secrets and untapped potential embedded within each word. In this evaluation, we shall explore the book is core themes, assess its distinct writing style, and delve into its lasting effect on the hearts and minds of those who partake in its reading experience.

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