



MOTOROLA SEMICONDUCTORS

3501 E. GILBERT STREET, AUSTIN, TEXAS 78761

8-Bit MPUs
8-Bit MCUs
8-Bit Peripherals

THE M6800 FAMILY

The M6800 microprocessor was the first MPU of the M6800 Family and still remains a highly cost-effective processor for a great many process-control and data-communications applications. It contains five powerful instructions and six different addressing modes plus it uses buffer capability, and a full range of compatible peripheral chips offer the widest possible latitude in system implementation. After years of field experience, the M6800 has earned an enviable reputation as one of the easiest-to-use processors available.

Moreover, to tailor the system to your specific needs (1) the lowest cost, the M6800 and its peripherals is available in three different packages, two different temperatures and three speed ranges.

This table is the right first device in the M6800 Family. The following microprocessor and microperipheral families are offshoots and enhancements of the M6800 architecture. It provides migration either the M6800 Family as a whole or some individual components.

Device	Function
M6800	MPU
M6801	MPU Latch Clock, Filter
M6802	MPU Latch Clock
M6803	128 x 8 Static RAM
M6804	Peripheral Interrupt Sequencer
M6805	Interrupt Interface Adapter
M6806	Priority Interrupt Controller
M6807	Memory Management Unit
M6808	CRP Controller with ROM
M6809	Floating Point ROM (COST-Effective CRP S.D.)
M6810	Programmable Timer
M6811	Pin-Data Controller
M6812	Serial Memory Address Controller
M6813	SRP Controller
M6814	ROM-IO-Two
M6815	Index Status Controller
M6816	Serial Memory Interface Adapter
M6817	Nonvolatile Serial Data Adapter
M6818	Advanced Data Link Controller
M6819	Data Memory Decoder
M6820	Signal Monitor
M6821	Signal Modulator
M6822	Serial Peripheral Interface Adapter

Extensive Digital Input, Analog Input and Special Functions

THE M6801 FAMILY: HIGH-PERFORMANCE MCUs

Frequency				Memory
1 MHz	1.5 MHz	2.5 MHz	3 MHz	
M6801	M6801-1	M6801-2	M6801-3	32 Kbytes, 128 Bytes RAM
M6802	M6802-1	—	—	32 Kbytes, 128 Bytes RAM, Expansion Bus
M6803	M6803-1	M6803-2	M6803-3	16 Kbytes, 128 Bytes RAM, Expansion Bus
M6804	M6804-1	—	—	16 Kbytes, 128 Bytes RAM, Expansion Bus
M6805	M6805-1	—	—	40 Kbytes, 128 Bytes RAM, Expansion Bus
M6806	M6806-1	—	—	40 Kbytes, 128 Bytes RAM, Expansion Bus
M6807	M6807-1	—	—	16 Kbytes, 128 Bytes RAM, Expansion Bus
M6808	M6808-1	—	—	32 Kbytes, 128 Bytes Dual-Power ROM, Expansion Bus
M6809	M6809-1	—	—	16 Kbytes, 128 Bytes, Bus-Full ROM, Expansion Bus

THE HMOS M6805 FAMILY PROCESSORS: MEDIUM-PERFORMANCE MCUs

Device	No. of Pins	Memory (Bytes)			I/O Lines	Special Features
		ROM	SRAM	RAM		
M6805-1	28	1K	—	64	24 Programmable, Bidirectional	Self-Check
M6805-16	28	1K	—	112	24 Programmable, Bidirectional	Self-Check, Internal RAM
M6805-16	40	2K	—	64	2 to 8 Inputs, 24 Programmable Bidirectional	1 to 4 Outputs, 8-Bit A/D, 8-Bit Counter
M6805-16	28	2.5K	—	64	2 General Purpose, 16 Programmable Bidirectional	P.L.L. Self-Check
M6805-16	40	2K	—	64	8 Inputs, 24 Programmable Bidirectional	Self-Check
M6805-16	28	—	1.5K	112	24 Programmable Bidirectional	Self-Programming, Self-Check
M6805-16	40	—	1.5K	112	2 to 8 Inputs, 24 Programmable Bidirectional	1 to 4 Outputs, 8-Bit A/D, Self-Programming, Self-Check
M6805-16	40	—	1.5K	112	8 Inputs, 24 Programmable Bidirectional	Self-Programming, Self-Check
M6805-16	40	—	1.5K	112	8 Inputs, 24 Programmable Bidirectional	Self-Check
M6805-16	28	—	1.5K	112	2 to 8 Inputs, 24 Programmable Bidirectional	1 to 4 Outputs, 8-Bit A/D, Self-Check

Motorola 68000 Datasheet

Motorola, Inc



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Intelligent Methods and Big Data in Industrial Applications Robert Bembenik, Łukasz Skonieczny, Grzegorz Protaziuk, Marzena Kryszkiewicz, Henryk Rybinski, 2018-05-18 The inspiration for this book came from the Industrial Session of the ISMIS 2017 Conference in Warsaw It covers numerous applications of intelligent technologies in various branches of the industry Intelligent computational methods and big data foster innovation and enable the industry to overcome technological limitations and explore the new frontiers Therefore it is necessary for scientists and practitioners to cooperate and inspire each other and use the latest research findings to create new designs and products As such the contributions cover solutions to the problems experienced by practitioners in the areas of artificial intelligence complex systems data mining medical applications and bioinformatics as well as multimedia and text processing Further the book shows new directions for cooperation between science and industry and facilitates efficient transfer of knowledge in the area of intelligent information systems

Embedded Systems Architecture Tammy Noergaard, 2012-12-31 Embedded Systems Architecture is a practical and technical guide to understanding the components that make up an embedded system s architecture This book is perfect for those starting out as technical professionals such as engineers programmers and designers of embedded systems and also for students of computer science computer engineering and electrical engineering It gives a much needed big picture for recently graduated engineers grappling with understanding the design of real world systems for the first time and provides professionals with a systems level picture of the key elements that can go into an embedded design providing a firm foundation on which to build their skills Real world approach to the fundamentals as well as the design and architecture process makes this book a popular reference for the daunted or the inexperienced if in doubt the answer is in here Fully updated with new coverage of FPGAs testing middleware and the latest programming techniques in C plus complete source code and sample code reference designs and tools online make this the complete package Visit the companion web site at <http://booksite.elsevier.com/9780123821966> for source code design examples data sheets and more A true introductory book provides a comprehensive get up and running reference for those new to the field and updating skills assumes no prior knowledge beyond undergrad level electrical engineering Addresses the needs of practicing engineers enabling it to get to the point more directly and cover more ground Covers hardware software and middleware in a single volume Includes a library of design examples and design tools plus a complete set of source code and embedded systems design tutorial materials from companion website

Embedded Systems Mr. Rohit Manglik, 2023-05-23 This book offers a detailed exploration of embedded systems focusing on key concepts methodologies and practical implementations relevant to modern engineering and technology practices

The Motorola MC68000 Jean Bacon, 1986

The Motorola MC68000 Microprocessor Family Thomas L. Harman, David T. Hein, 1996 ndice Introduction to the Motorola Microprocessors and Controllers Microcomputer and Microprocessor Characteristics Representation of Numbers and Characters Introduction to

the M68000 Family MC68000 Assembly Language and Basic Instructions Data Transfer Program Control and Subroutines
 Arithmetic Operations Logical and Bit Operations Programming Techniques System Operation Exception Processing
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 Language Characteristics of the MC68000 MC68008 and MC68010 Appendix V Comparison of 68000 Family Processors
 Answers to Selected Exercises Index

Junk Box Arduino James R. Strickland, 2016-07-04 We all hate to throw electronics away Use your 5 volt Arduino and have fun with them instead Raid your electronics junk box to build the Cestino Arduino compatible board and nine other electronics projects from a logic probe to a microprocessor explorer and learn some advanced old school techniques along the way Don't have a well stocked junk box No problem Nearly all the components used in these projects are still available and cheap at major electronic parts houses worldwide Junk Box Arduino is the ultimate have fun while challenging your skills guide for Arduino hackers who've gone beyond the basic tutorials and are ready for adventures in electronics Bonus materials include all the example sketches the Cestino core and bootloader source code and links to suppliers for parts and tools Bonus materials include extensions to the Cestino Sourceforge links for updated code and all the source code for the projects

MC68000 16-bit Microprocessor Motorola, Inc, 1982 For Design Engineers Software Architects Computer Designers a Guide to Completing Software Systems Using Motorola's MC68000 Family of Microprocessors

The 68000 Microprocessor James L. Antonakos, 2004 Designed to demystify the Motorola 68000 microprocessor its hardware and software this detailed reference leads users on an in depth hands on exploration of more than 75 different applications and then guides them through the construction and programming of their own working single board 68000 system Chapter topics cover microprocessor based systems the 68000 microprocessor software details of the 68000 exception processing an introduction to data structures and programming the 68000 hardware details of the 68000 memory system design I O system design advanced programming using 68000 peripherals building a working 68000 system an introduction to the advanced 680x0 series microprocessors and microcontrollers For programmers and microcomputer network technicians and engineers

The 68000 Microprocessor Andrew M. Veronis, 2012-12-06 The Motorola MC68000 family of microprocessors is undoubtedly a revolutionary set of devices The MC68000 is the first advanced 16 bit microprocessor with a 32 bit internal architecture and the first with 16 megabyte nonsegmented direct memory addressing The processor's six basic addressing modes are equivalent to 14 when one considers all of the variations among these modes Combined with the device's data and instruction types the modes provide more than 1000 useful instructions The book you are about to study has been developed as an aid to the hardware designer and as a supplement to the Motorola seminars on the 68000 microprocessor The text includes a detailed description of the MC68000 and two complete systems that show how this processor can be interfaced to the outside world The book follows a top down approach

A brief history of microprocessors is provided first Chapter 2 details the MC68000 by describing its registers control lines and capabilities Chapter 3 introduces a small MC68000 based system Although this system is characterized in the book as hypothetical it is indeed the Educational Computer Board used in the various Motorola seminars The addressing modes and instructions are explained in Chapter 4 which includes helpful hints on how instructions can be used Chapter 5 provides an in depth description of additional instructions and numerous examples Chapter 6 discusses exception handling and interrupts

M68000 8-/16-/32-bit Microprocessors Motorola, Inc,1990 Provides manufacturer s hardware and performance data on the 68000 microprocessor series The book also examines data organization and sets out the capabilities for each processor and enumerates specifications and operating details There is also a discussion of the hardware architecture *Assembly and Assemblers* G. W. Gorsline,1988 *Welding Journal* ,1987 **The 68000 Microprocessor** James L. Antonakos,1993

M68000 8-/16-/32-bit Microprocessors Motorola, Inc,1986 *The 68000 Microprocessor Family* Michael A. Miller,1992 *Electronic Design* ,1988 *M68000 8-/16-/32-Bit Microprocessors* ,1988 **The 68000 Microprocessor** Walter A. Triebel,Avtar Singh,1986 **68000 Microprocessor Handbook** William D. Cramer,Gerry Kane,1986 **Introduction to 6800/68000 Microprocessors** Frederick F. Driscoll,1987

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