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    Muqarnas Gülru Necipo?lu,Karen Leal,2010-11-11 The articles in Muqarnas 27 address topics such as spolia in medieval Islamic architecture Islamic coinage in the seventh century the architecture of the Alhambra from an environmental perspective and Ottoman Mamluk gift exchange in the fifteenth century The volume also features a new section entitled Notes and Sources with pieces highlighting primary sources such as Akbar s Kath sarits gara Muqarnas An Annual on the Visual Cultures of the Islamic World is sponsored by the Aga Khan Program for Islamic Architecture at Harvard University and the Massachusetts Institute of Technology Cambridge Massachusetts **InfoWorld** ,2006-08-28 InfoWorld is targeted to Senior IT professionals Content is segmented into Channels and Topic Centers InfoWorld also celebrates people companies and projects *Microarchitecture of Network-on-Chip Routers* Giorgos Dimitrakopoulos,Anastasios Psarras,Ioannis Seitanidis,2014-08-27 This book provides a unified overview of network on chip router micro architecture the corresponding design opportunities and challenges and existing solutions to overcome these challenges The discussion focuses on the heart of a NoC the NoC router and how it interacts with the rest of the system Coverage includes both basic and advanced design techniques that cover the entire router design space including router organization flow control pipelined operation buffering architectures as well as allocators structure and algorithms Router micro architectural options are presented in a step by step manner beginning from the basic design principles Even highly sophisticated design alternatives are categorized and broken down to simpler pieces that can be understood easily and analyzed This book is an invaluable reference for system architecture circuit and EDA researchers and developers who are interested in understanding the overall picture of NoC routers architecture the associated design challenges and the available solutions *The Microarchitecture of Pipelined and Superscalar Computers* Amos R. Omondi,2013-03-09 This book is intended to serve as a textbook for a second course in the im plementation Le microarchitecture of computer architectures The subject matter covered is the collection of techniques that are used to achieve the highest performance in single processor machines these techniques center the exploitation of low level parallelism temporal and spatial in the processing of machine instructions The target audience consists students in the final year of an undergraduate program or in the first year of a postgraduate program in computer science computer engineering or electrical engineering professional computer designers will also also find the book useful as an introduction to the topics covered Typically the author has used the material presented here as the basis of a full semester undergraduate course or a half semester post graduate course with the other half of the latter devoted to multiple processor machines The background assumed of the reader is a good first course in computer architecture and implementation to the level in say Computer Organization and Design by D Patterson and H Hennessy and familiarity with digital logic design The book consists of eight chapters The first chapter is an introduction to all of the main ideas that the following chapters cover in detail the topics covered are the main forms of pipelining used in high performance uniprocessors a taxonomy of the space of pipelined

processors and performance issues It is also intended that this chapter should be readable as a brief stand alone survey

Embracing Microservices Design Ovais Mehboob Ahmed Khan,Nabil Siddiqui,Timothy Oleson,Mark Fussell,2021-10-29 Develop microservice based enterprise applications with expert guidance to avoid failures and technological debt with the help of real world examples Key FeaturesImplement the right microservices adoption strategy to transition from monoliths to microservicesExplore real world use cases that explain anti patterns and alternative practices in microservices developmentDiscover proven recommendations for avoiding architectural mistakes when designing microservicesBook Description Microservices have been widely adopted for designing distributed enterprise apps that are flexible robust and fine grained into services that are independent of each other There has been a paradigm shift where organizations are now either building new apps on microservices or transforming existing monolithic apps into microservices based architecture This book explores the importance of anti patterns and the need to address flaws in them with alternative practices and patterns You ll identify common mistakes caused by a lack of understanding when implementing microservices and cover topics such as organizational readiness to adopt microservices domain driven design and resiliency and scalability of microservices The book further demonstrates the anti patterns involved in re platforming brownfield apps and designing distributed data architecture You ll also focus on how to avoid communication and deployment pitfalls and understand cross cutting concerns such as logging monitoring and security Finally you ll explore testing pitfalls and establish a framework to address isolation autonomy and standardization By the end of this book you ll have understood critical mistakes to avoid while building microservices and the right practices to adopt early in the product life cycle to ensure the success of a microservices initiative What you will learnDiscover the responsibilities of different individuals involved in a microservices initiativeAvoid the common mistakes in architecting microservices for scalability and resiliencyUnderstand the importance of domain driven design when developing microservicesIdentify the common pitfalls involved in migrating monolithic applications to microservicesExplore communication strategies along with their potential drawbacks and alternativesDiscover the importance of adopting governance security and monitoringUnderstand the role of CI CD and testingWho this book is for This practical microservices book is for software architects solution architects and developers involved in designing microservices architecture and its development who want to gain insights into avoiding pitfalls and drawbacks in distributed applications and save time and money that might otherwise get wasted if microservices designs fail Working knowledge of microservices is assumed to get the most out of this book _Object-Oriented Design Knowledge: Principles, Heuristics and Best Practices_ Garz s, Javier,Piattini, Mario,2006-07-31 The software engineering community has advanced greatly in recent years and we currently have numerous defined items of knowledge such as standards methodologies methods metrics techniques languages patterns knowledge related to processes concepts etc The main objective of this book is to give a unified and global vision about Micro Architectural Design Knowledge analyzing the main

techniques experiences and methods Provided by publisher   The X86 Microprocessors: Architecture and Programming (8086 to Pentium) Lyla B. Das,2010-09   Computer Architecture John L. Hennessy,David A. Patterson,2006-11-03 The era of seemingly unlimited growth in processor performance is over single chip architectures can no longer overcome the performance limitations imposed by the power they consume and the heat they generate Today Intel and other semiconductor firms are abandoning the single fast processor model in favor of multi core microprocessors chips that combine two or more processors in a single package In the fourth edition of Computer Architecture the authors focus on this historic shift increasing their coverage of multiprocessors and exploring the most effective ways of achieving parallelism as the key to unlocking the power of multiple processor architectures Additionally the new edition has expanded and updated coverage of design topics beyond processor performance including power reliability availability and dependability CD System Requirements PDF Viewer The CD material includes PDF documents that you can read with a PDF viewer such as Adobe Acrobat or Adobe Reader Recent versions of Adobe Reader for some platforms are included on the CD HTML Browser The navigation framework on this CD is delivered in HTML and JavaScript It is recommended that you install the latest version of your favorite HTML browser to view this CD The content has been verified under Windows XP with the following browsers Internet Explorer 6 0 Firefox 1 5 under Mac OS X Panther with the following browsers Internet Explorer 5 2 Firefox 1 0 6 Safari 1 3 and under Mandriva Linux 2006 with the following browsers Firefox 1 0 6 Konqueror 3 4 2 Mozilla 1 7 11 The content is designed to be viewed in a browser window that is at least 720 pixels wide You may find the content does not display well if your display is not set to at least 1024x768 pixel resolution Operating System This CD can be used under any operating system that includes an HTML browser and a PDF viewer This includes Windows Mac OS and most Linux and Unix systems Increased coverage on achieving parallelism with multiprocessors Case studies of latest technology from industry including the Sun Niagara Multiprocessor AMD Opteron and Pentium 4 Three review appendices included in the printed volume review the basic and intermediate principles the main text relies upon Eight reference appendices collected on the CD cover a range of topics including specific architectures embedded systems application specific processors some guest authored by subject experts   **In Detail, Small Structures** Christian Schittich,2012-12-10 Shelters kiosks snack bars market stalls bus stops telephone booths toilets advertising columns ticket booths mobile tents or housing units emergency shelters tourist information booths this list of small autonomously functioning buildings could be expanded almost infinitely Small buildings shape our daily lives they are found at the nearest street corner they are present and indispensable but as architecture they attract our attention only rarely Yet these small structures occupy a definite place in the infrastructure of the city Rather than focusing on the large attractions of architecture architects find many potential ways to ensure the quality of everyday design hidden in these small sometimes charming necessities This volume in the DETAIL series spans the arc between architecture and product design since not infrequently small buildings are located precisely in the area of

tension between these two professions and their successful realization is evident in the details of their construction

   Network-on-Chip Architectures Chrysostomos Nicopoulos,Vijaykrishnan Narayanan,Chita R. Das,2009-09-18 2 The Cell Processor from Sony Toshiba and IBM STI 3 and the Sun UltraSPARC T1 formerly codenamed Niagara 4 signal the growing popularity of such systems Furthermore Intel s very recently announced 80 core TeraFLOP chip 5 exemplifies the irreversible march toward many core systems with tens or even hundreds of processing elements 1 2 The Dawn of the Communication Centric Revolution The multi core thrust has ushered the gradual displacement of the computati centric design model by a more communication centric approach 6 The large sophisticated monolithic modules are giving way to several smaller simpler p cessing elements working in tandem This trend has led to a surge in the popularity of multi core systems which typically manifest themselves in two distinct incarnations heterogeneous Multi Processor Systems on Chip MPSoC and homogeneous Chip Multi Processors CMP The SoC philosophy revolves around the technique of Platform Based Design PBD 7 which advocates the reuse of Intellectual Property IP cores in flexible design templates that can be customized accordingly to satisfy the demands of particular implementations The appeal of such a modular approach lies in the substantially reduced Time To Market TTM incubation period which is a direct outcome of lower circuit complexity and reduced design effort The whole system can now be viewed as a diverse collection of pre existing IP components integrated on a single die   *Digital Design and Computer Architecture* David Harris,Sarah Harris,2010-07-26 Digital Design and Computer Architecture is designed for courses that combine digital logic design with computer organization architecture or that teach these subjects as a two course sequence Digital Design and Computer Architecture begins with a modern approach by rigorously covering the fundamentals of digital logic design and then introducing Hardware Description Languages HDLs Featuring examples of the two most widely used HDLs VHDL and Verilog the first half of the text prepares the reader for what follows in the second the design of a MIPS Processor By the end of Digital Design and Computer Architecture readers will be able to build their own microprocessor and will have a top to bottom understanding of how it works even if they have no formal background in design or architecture beyond an introductory class David Harris and Sarah Harris combine an engaging and humorous writing style with an updated and hands on approach to digital design Unique presentation of digital logic design from the perspective of computer architecture using a real instruction set MIPS Side by side examples of the two most prominent Hardware Design Languages VHDL and Verilog illustrate and compare the ways the each can be used in the design of digital systems Worked examples conclude each section to enhance the reader s understanding and retention of the material   *Environmental Health and Biomedicine* C. A. Brebbia,2011 Following on the success of the first conference the Wessex Institute of Technology is convening the Second International Conference on Physical Coastal Processes Management and Engineering This book contains papers to be presented at that Conference Coastal zone dynamics involve distinctive features that stem from both near shore hydrodynamics and the complex local

behaviour of the atmosphere that is affected by the irregularity of the coastal topography and variations in land sea surface roughness and thermal properties Complex interactions occur between the atmosphere ocean and land leading to large temporal and spatial differences in air sea exchange processes and wind strength and direction Recreational and tourism demand on coastal areas activities makes increased shore and beach protection necessary Coastlines are often subjected to direct impact of wind swell and storm wave activity Many other physical phenomena such as tides and associated currents long waves and storm surges also affect the dynamic behaviour of the coastal zone With the increase in extreme events due to climate change the role of extreme events in changing coastal zones needs to be considered The International Conference will consider also of these and will cover such topics as Wave modelling Wave transformation hydrodynamics Extreme events and sea level rise Sea defences Interaction between coastal defences and processes Energy recovery Hydrodynamic forces Sediment transport and erosion Pollution and dispersion Planning and beach design Coastal geomorphology Coastal processes and navigation Coastal processes and GIS Bio physical coastal processes and Great Lakes problems The book will be of interest to engineers and government officials involved with coastal zone management and development

Architecture of Computing Systems Georgios Goumas,Sven Tomforde,Jürgen Brehm,Stefan Wildermann,Thilo Pionteck,2023-08-25 T his book constitutes the proceedings of the 36th International Conference on Architecture of Computing Systems ARCS 2023 which took place in Athens Greece in June 2023 The 18 full papers in this volume were carefully reviewed and selected from 35 submissions ARCS provides a platform covering newly emerging and cross cutting topics such as autonomous and ubiquitous systems reconfigurable computing and acceleration neural networks and artificial intelligence The selected papers cover a variety of topics from the ARCS core domains including energy efficiency applied machine learning hardware and software system security reliable and fault tolerant systems and organic computing Back to top InfoWorld ,2006-07-10 InfoWorld is targeted to Senior IT professionals Content is segmented into Channels and Topic Centers InfoWorld also celebrates people companies and projects Computerworld ,1981-12-21 For more than 40 years Computerworld has been the leading source of technology news and information for IT influencers worldwide Computerworld s award winning Web site Computerworld com twice monthly publication focused conference series and custom research form the hub of the world s largest global IT media network **FPGA Algorithms and Applications for the Internet of Things** Sharma, Preeti,Nair, Rajit,2020-03-30 In the research area of computer science practitioners are constantly searching for faster platforms with pertinent results With analytics that span environmental development to computer hardware emulation problem solving algorithms are in high demand Field Programmable Gate Array FPGA is a promising computing platform that can be significantly faster for some applications and can be applied to a variety of fields FPGA Algorithms and Applications for the Internet of Things provides emerging research exploring the theoretical and practical aspects of computable algorithms and applications within robotics and electronics development Featuring coverage on a

broad range of topics such as neuroscience bioinformatics and artificial intelligence this book is ideally designed for computer science specialists researchers professors and students seeking current research on cognitive analytics and advanced computing **Computer Aided Verification** Nicolas Halbwachs,Doron Peled,2003-07-31 This book constitutes the refereed proceedings of the 11th International Conference on Computer Aided Verification CAV 99 held in Trento Italy in July 1999 as part of FLoC 99 The 34 revised full papers presented were carefully reviewed and selected from a total of 107 submissions Also included are six invited contributions and five tool presentations The book is organized in topical sections on processor verification protocol verification and testing infinite state spaces theory of verification linear temporal logic modeling of systems symbolic model checking theorem proving automata theoretic methods and abstraction **FCS Computer Hardware & Software L3** Adrian Cook, Vaughan Van Dyk, Angela Du Preez,2008 **Transition of HPC Towards Exascale Computing** Erik H. D'Hollander,Jack J. Dongarra,Ian Foster,Lucio Grandinetti,2013-10-15 The US Europe Japan and China are racing to develop the next generation of supercomputers exascale machines capable of 10 to the 18th power calculations a second by 2020 But the barriers are daunting the challenge is to change the paradigm of high performance computing The 2012 biennial high performance workshop held in Cetraro Italy in June 2012 focused on the challenges facing the computing research community to reach exascale performance in the next decade This book presents papers from this workshop arranged into four major topics energy scalability new architectural concepts and programming of heterogeneous computing systems Chapter 1 introduces the status of present supercomputers which are still about two orders of magnitude separated from the exascale mark Chapter 2 examines energy demands a major limiting factor of today s fastest supercomputers the quantum leap in performance required for exascale computing will require a shift in architectures and technology In Chapter 3 scalable computer paradigms for dense linear algebra on massive heterogeneous systems are presented and Chapter 4 discusses architectural concepts Finally Chapter 5 addresses the programming of heterogeneous systems This book will be of interest to all those wishing to understand how the development of modern supercomputers is set to advance in the next decade Semantic Web Services Rudi Studer,Stephan Grimm,Andreas Abecker,2007-05-23 In just a few years service oriented architectures SOA and Web services not only gained considerable interest in computer science research they were also taken up with unanimity by all major international players in the IT industry However and in spite of all existing standards in most SOA applications much human intervention is still required for example to interpret the semantics of informal descriptions or to harmonize incompatible data schemata Semantic Web services combine Web services communication technology with the intelligent processing of ontology based metadata to achieve highly integrated enterprise application integration scenarios for service look up schema matching or protocol negotiation for example Rudi Studer and his team deliver a self contained compendium about this exciting field starting with the basic standards and technologies and also including advanced applications in eGovernment and eHealth The

contributions provide both the theoretical background and the practical knowledge necessary to understand the essential ideas and to design new cutting edge applications They address computer science students as well as researchers in academia and industry who need a concise introduction and state of the art survey of current research and the book can easily be used as the basis of a specialized course on Web services or Semantic Web applications In addition IT professionals will be able to assess the potential and possible benefits of this new technology

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