



**MOTOROLA INC.**  
Communications  
Sector

# RECEIVER

MODEL SERIES TRD6170A, TRD6180A, AND TRD6190A



*Receiver Model Table*

Model	Frequency (MHz)	Description	Application
TRD6171A	132-150.8	Multi-Frequency 10.7 MHz I-F, Non-Filtered	Used with Single Receiver Stations
TRD6172A	146-174		
TRD6181A	132-150.8	Multi-Frequency 10.7 MHz I-F, Filtered	Used with 2-Receiver Stations and Repeater Stations
TRD6182A	146-174		
TRD6191A	132-150.8	Multi-Frequency 10.8 MHz I-F, Filtered	Used with 2 Receiver Stations Where Shifted I-F is Required.
TRD6192A	146-174		

## SPECIFICATIONS

Input Impedance	50 ohms		
Number of Channels	1, 2, 3, or 4		
Frequency Separation	2.0 MHz		
I-F Frequency	10.7 MHz or 10.8 MHz		
EIA Modulation Acceptance	± 7 kHz Minimum		
Frequency Stability	± .0005% from - 30 °C to + 60 °C ambient ( + 25 °C reference) ± .0002% Optional*		
Channel Spacing	20 kHz, 25/30 kHz		
Sensitivity:		With Preamp**	Without Preamp
20-dB Quieting	Less Than	.25 µV	.50 µV
EIA SINAD	Less Than	.20 µV	.35 µV
Selectivity:			
EIA SINAD	20 kHz Channel 25/30 kHz Channel	85 dB 95 dB	90 dB 100 dB
Intermodulation:			
EIA SINAD	20 kHz Channel 25/30 kHz Channel	75 dB (Minimum) 80 dB (Minimum)	80 dB (Minimum) 85 dB (Minimum)
Spectrum and Image Rejection		100 dB (Minimum)	100 dB (Minimum)

\* Available with option C621, C622, C623, C624.

\*\* Optional Model HLD4050A, RF Preamp.

## 1. DESCRIPTION

1.1 These receivers are fully transistorized units that receive FM signals on one to four crystal-controlled frequencies. In a multi-frequency receiver, only one frequency can be received at a time.

1.2 Each receiver includes an rf preselector, mixer, local oscillator injection circuitry, high gain selective i-f stages, quadrature detector, audio preamplifier, and a low-ripple 9.5 volt regulator. The receiver develops a low noise audio signal from a frequency

modulated "on-channel" rf carrier in the 132-174 MHz range. An optional rf preamplifier may be used with any of these receivers.

1.3 All circuits are constructed on a single plug-in circuit board which is easily accessible for servicing. The receiver plugs into the backplane interconnect board which provides all dc, audio, and rf connections thereby eliminating all interconnecting wiring. All alignment points are accessible through the top of the rf compartment cover.

RECEIVER

**technical writing services**

1301 E. Algonquin Road, Schaumburg, IL 60196

# Motorola Technical Manuals

**R Bogdan**



## **Motorola Technical Manuals:**

**Technical Manual** United States Department of the Army, 1982      **Microprocessors and Microcomputer-Based System Design** Mohamed Rafiquzzaman, 2021-11-01 Microprocessors and Microcomputer Based System Design Second Edition builds on the concepts of the first edition It discusses the basics of microprocessors various 32 bit microprocessors the 8085 microprocessor the fundamentals of peripheral interfacing and Intel and Motorola microprocessors This edition includes new topics such as floating point arithmetic Program Array Logic and flash memories It covers the popular Intel 80486 80960 and Motorola 68040 as well as the Pentium and PowerPC microprocessors The final chapter presents system design concepts applying the design principles covered in previous chapters to sample problems      **Motorola Xoom: The Missing Manual** Preston Gralla, 2011-05-26 Looks at the features and functions of the Motorola Xoom covering such topics as accessing the Internet setting up an email account using apps playing games playing and managing music watching videos and more      **Technical Manual** United States. War Department, 1945      **Microprocessor Theory and Applications with 68000/68020 and Pentium** M. Rafiquzzaman, 2008-09-22 MICROPROCESSOR THEORY AND APPLICATIONS WITH 68000 68020 AND PENTIUM A SELF CONTAINED INTRODUCTION TO MICROPROCESSOR THEORY AND APPLICATIONS This book presents the fundamental concepts of assembly language programming and system design associated with typical microprocessors such as the Motorola MC68000 68020 and Intel Pentium It begins with an overview of microprocessors including an explanation of terms the evolution of the microprocessor and typical applications and goes on to systematically cover Microcomputer architecture Microprocessor memory organization Microprocessor Input Output I O Microprocessor programming concepts Assembly language programming with the 68000 68000 hardware and interfacing Assembly language programming with the 68020 68020 hardware and interfacing Assembly language programming with Pentium Pentium hardware and interfacing The author assumes a background in basic digital logic and all chapters conclude with a Questions and Problems section with selected answers provided at the back of the book Microprocessor Theory and Applications with 68000 68020 and Pentium is an ideal textbook for undergraduate and graduate level courses in electrical engineering computer engineering and computer science An instructor s manual is available upon request It is also appropriate for practitioners in microprocessor system design who are looking for simplified explanations and clear examples on the subject Additionally the accompanying Website which contains step by step procedures for installing and using Ide 68k21 68000 68020 and MASM32 Olly Debugger Pentium software provides valuable simulation results via screen shots

**Fundamentals of Digital Logic and Microcomputer Design** M. Rafiquzzaman, 2005-06-06 Fundamentals of Digital Logic and Microcomputer Design has long been hailed for its clear and simple presentation of the principles and basic tools required to design typical digital systems such as microcomputers In this Fifth Edition the author focuses on computer design at three levels the device level the logic level and the system level Basic topics are covered such as number systems

and Boolean algebra combinational and sequential logic design as well as more advanced subjects such as assembly language programming and microprocessor based system design Numerous examples are provided throughout the text Coverage includes Digital circuits at the gate and flip flop levels Analysis and design of combinational and sequential circuits Microcomputer organization architecture and programming concepts Design of computer instruction sets CPU memory and I O System design features associated with popular microprocessors from Intel and Motorola Future plans in microprocessor development An instructor s manual available upon request Additionally the accompanying CD ROM contains step by step procedures for installing and using Altera Quartus II software MASM 6 11 8086 and 68asmsim 68000 provides valuable simulation results via screen shots Fundamentals of Digital Logic and Microcomputer Design is an essential reference that will provide you with the fundamental tools you need to design typical digital systems

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