### Compilers

- \*\* What is a scope? What is a scope in PL#? In C? An area of program where declaration has effect. In C. ()
- What is a symbol table? What operations would it support? Data structure used by compilers to store info about identifiers. Lookup'add ++ How is a symbol table used to check declarations and uses of identifiers? Goes thru data at, verifying declaration within scope

2. Code generation

- What is an IR? Intermediate representation, Modularizes compilers and code generation between larges
- What from a name's use is needed to generate code? Lex Address:

  What is the general way that the code for a code generator is written? Follow gramma of the AST's:
- + How are ARs used on the runtime stack? On top at each poor, call
- \* Why are identifier uses important for code generation? Provide a way for the compiler to refer to var, consts, functs, etc appropriately
- + How is the lexical address of a variable (or constant) used to generate code? Its scope determines where declared variables are effective, and officel lets the code are know where in the scope to look
- What does the generated code for obtaining the value of a variable look like in the stack machine? Lood variable (then read?)
- + Where does compiled code put the value of an expression in the stack machine? Top of stack.
- How are constant declarations translated into machine code in the stack machine? Value from AST is pushed with LIT
- + How are variable declarations translated into machine code? INC allocates space for it on stack
- \* Where are globally visible constants and variables allocated? Program memory
- How are assignment statements compiled? What code do they generate? store the top of the stack's value into the address at the "stack[SP-2]+offset+LINK\_SIZE, and then pop twice
- + How are if-then-else statements compiled? What code do they generate? Conditional jump around then and else
- if the machine's jump instructions required absolute addresses?
   Yes, in a first pass determine address, use a second pass to fix the instruction addresses with information from the label
- + How are while-loops compiled? What code do they generate? generate code for the condition; jump conditionally 2 (around a next jump), jump around the body (to exit), generate code for the body of the loop, jump back to beginning of condition.
- How is the starting address of a procedure obtained by a call instruction? Look up procedure in in the data structure its stored
- Why does the runtime stack need to be trimmed before executing a
- return instruction? The prev. functions stack frame must be existered.

  \* With static scoping, what does the BP register point to in an AR for a nexted scope? Beginning of the current activation record for the

### current nested scope B. Assembly Language

- What is an assembly language? Strong correspond. Between lang and machine code
- What are the goals of assembly language? Help tedium of machine code and help communication between people (if's and dec--bin)
- ++ How does assembly language differ from a (high-level)
  programming language? Statements are one instruction, expressions
  must be explicitly programmed, names are abstractions of locations.
- How does an assembler translate forward jumps (to a label)? Two jumps, jump 1 counts instructions, determines label addresses, pass2 check, that labels are defined, generate machine code.
- + What is ELF? Executable file
- What are the sections of an ELF file? What information is in each section? Fest (instructions), Data (data in binary), relocation (identifies locations modifies adjustment when program is moved in memory.
   How does relocation of executable files work? Identifies parts of instructions that need offset, added if starting add is changed to 0 volfset.
   Relocations.
- ++ What is a linker? What does a linker do? Combine object files eg(program and library)
- Why is a linker useful for programming? compartmentalizing
   How does a linker support separate compilation? Source files -object files -- object files combined into executable
- How does a linker support program libraries? allowing developers to link their programs with external libraries.

- ++ What is the difference between static linking and dynamic linking? Static happens before running a program, dynamic during.
- Which kind of linking is used to link together the compiled files of a user's program? Dynam?
- Which kind of linking is performed to link a user's program to a shared library? Dynam

### 2. Insudans

- ++ What is a leader? places Program in memory
- What are the types of loaders? When is each type used? Absolute loader puts program text into specific locs. Bootstrap loads the OS.
   Belocating puts program anywhere there is space.
- virtual memory, do we still need a relocating loader? Yes.

### E. Operating System (OS)

- + What are the goals of an operating system? Make easy run programs, interface between programs and bardware, manage resources + What are the main techniques that are used to implement an OS? Monodithic(no boundaries), Layered (can only use layer below), Micro-Kernel (layers but with kernel as small as possible.
- + What would happen if an OS was just a bunch of libraries?No central control

### B. processes

- \*\* What is a process? Program being run (or waiting to run)
- + Why does an OS have processes? Tell press that it has all resources
- . How does a process differ from a program? Program isnt being non
- \* How does a program become a program? Program issued only in the PAS.nm
- What states can a process be in? Ready, Running, Waiting, Suspended, End, Abend
- What is a PCB? What information does it typically story? Process control block, processID, mode, priority, state, code, BPSP, PC
- Where do a process's permissions come from? User/OS
- 2. imberruget
- How does an OS enable sharing of resources? interrupts.
- \* What is needed for an OS to enforce sharing? OS > XX permissions
- + Why is an interrupt handling mechanism a useful idea in an OS?
- Allows for interruptions that require immediate attention
- \* What is a trap interrupt? Error, division by 0, arith overflow
- + What does an interrupt handler do?Save our process, jump to code
- What is an EO interrupt?EO device needs to talk to CPU.
- What is a trap table (aka an interrupt vector)? Array of inter. addrs.
   J. limited direct execution
- ++ What is limited direct execution? Runs programs efficiently, shares resources, prevents bad behavior
- What is needed from the hardware (the ISA) to make limited direct execution work? Mode flag, address fence
- 4. system calls
- \* What is a system call? Normal library function call, saves state, pushes arguments on stack, sets system call number, executes interrupt. How are system calls implemented in an OS? Saves process state on keenel stack, jump through trap table, restore process state.
- If a user mode process cannot execute privileged instructions how can it do any UO? System call request help from kernel
- Is your program's process executed in kernel mode? No
- + If your program is doing I/O, does that mean that the process you are running is executing privileged instructions?
- + What is memory protection? Why is it needed? Stops user from finding interrupt handler location and running arbitrary code
- + What does a "segmentation fault" mean for a C program? Memory protection violation

### S. thereads.

- What is a thread? Why is it a useful concept for an OS? Smaller process within a process. Simultaneous execution of threads > big process.
   Door each thread need its own stack? Yes.
- + What is a race condition? 2 threads changing data at same time
- What is a critical section? Portion of program that cant be used by multiple threads
- What is mutual exclusion? How is it used in concurrent programming? Prevents multiple threads from accessing shared resource
   What is serial exception? Sequential exception. Emures predictability
- What is atomic execution? Why is that a useful concept? Sequence of operations that can't be interrupted. Ensures certain oper, get curcuted

# **Os Study Guide**

## **R. Dennis Gibbs**



Unveiling the Magic of Words: A Overview of "Os Study Guide"

In a global defined by information and interconnectivity, the enchanting power of words has acquired unparalleled significance. Their capability to kindle emotions, provoke contemplation, and ignite transformative change is really awe-inspiring. Enter the realm of "**Os Study Guide**," a mesmerizing literary masterpiece penned by a distinguished author, guiding readers on a profound journey to unravel the secrets and potential hidden within every word. In this critique, we shall delve in to the book is central themes, examine its distinctive writing style, and assess its profound impact on the souls of its readers.

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