

LMK60XX High-Performance Low Jitter Oscillator

1 Features

- Low Noise, High Performance
 - Jitter: 150 fs RMS typical $F_{out} > 100$ MHz
 - PSRR: -60 dBc, Robust Supply Noise Immunity
- Supported Output Format
 - LVPECL and LVDS up to 800 MHz
 - HCSL up to 400 MHz
- Total Frequency Tolerance of ± 50 ppm (LMK60X2) and ± 25 ppm (LMK60X0)
- 3.3-V Operating Voltage
- Industrial Temperature Range (-40°C to $+85^{\circ}\text{C}$)
- 7-mm \times 5-mm 6-pin Package That is Pin-Compatible With Industry Standard 7050 XO Package

2 Applications

- High-Performance Replacement for Crystal-, SAW-, or Silicon-based Oscillators
- Switches, Routers, Network Line Cards, Base Band Units (BBU), Servers, Storage/SAN
- Test and Measurement
- Medical Imaging
- FPGA, Processor Attach

3 Description

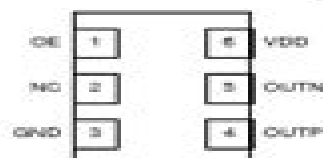
The LMK60XX device is a low jitter oscillator that generates a commonly used reference clock. The device is pre-programmed in factory to support any reference clock frequency; supported output formats are LVPECL, and LVDS up to 800 MHz, and HCSL up to 400 MHz. Internal power conditioning provide excellent power supply ripple rejection (PSRR), reducing the cost and complexity of the power delivery network. The device operates from a single 3.3-V $\pm 5\%$ supply.

Device Information⁽¹⁾

PART NUMBER	OUTPUT FREQ (MHz) AND FORMAT	TOTAL FREQ STABILITY (ppm)	PACKAGE / SIZE
LMK60E2-150M	150 LVPECL	± 50	6-pin GFM, 7 mm \times 5 mm
LMK60E0-156257	156.257 LVPECL	± 25	
LMK60A0-148351	148.33251 LVDS	± 25	
LMK60A0-148M	148.3 LVDS	± 25	

(1) For all available packages, see the orderable addendum at the end of the data sheet.

Pinout



Oscillator Texas Instruments

Vinod Kumar Khanna



Oscillator Texas Instruments:

The Automatic Standard Magnetic Observatory Leroy Romney Alldredge, I. Saldukas, 1966 **Microcontroller Programming and Interfacing with Texas Instruments MSP430FR2433 and MSP430FR5994** Steven F. Barrett, Daniel J. Pack, 2022-06-01 This book provides a thorough introduction to the Texas Instruments MSP430TM microcontroller The MSP430 is a 16 bit reduced instruction set RISC processor that features ultra low power consumption and integrated digital and analog hardware Variants of the MSP430 microcontroller have been in production since 1993 This provides for a host of MSP430 products including evaluation boards compilers software examples and documentation A thorough introduction to the MSP430 line of microcontrollers programming techniques and interface concepts are provided along with considerable tutorial information with many illustrated examples Each chapter provides laboratory exercises to apply what has been presented in the chapter The book is intended for an upper level undergraduate course in microcontrollers or mechatronics but may also be used as a reference for capstone design projects Also practicing engineers already familiar with another microcontroller who require a quick tutorial on the microcontroller will find this book very useful This second edition introduces the MSP EXP430FR5994 and the MSP430 EXP430FR2433 LaunchPads Both LaunchPads are equipped with a variety of peripherals and Ferroelectric Random Access Memory FRAM FRAM is a nonvolatile low power memory with functionality similar to flash memory *Sinusoidal Oscillators and Waveform Generators using Modern Electronic Circuit Building Blocks* Raj Senani, D. R. Bhaskar, V. K. Singh, R. K. Sharma, 2015-11-26 This book serves as a single source reference to sinusoidal oscillators and waveform generators using classical as well as a variety of modern electronic circuit building blocks It provides a state of the art review of a large variety of sinusoidal oscillators and waveform generators and includes a catalogue of over 600 configurations of oscillators and waveform generators describing their relevant design details and salient performance features limitations The authors discuss a number of interesting open research problems and include a comprehensive collection of over 1500 references on oscillators and non sinusoidal waveform generators relaxation oscillators Offers readers a single source reference to everything connected to sinusoidal oscillators and waveform generators using classical as well as modern electronic circuit building blocks Provides a state of the art review of a large variety of sinusoidal oscillators and waveform generators Includes a catalog of over 600 configurations of oscillators and waveform generators with their relevant design details and their salient performance features limitations **Texas Instruments Technical Journal** ,1998 *Technical Bulletin* ,1963 **Robert Lacoste's The Darker Side** Robert Lacoste, 2009-11-27 Robert Lacoste's The Darker Side column has quickly become a must read among Circuit Cellar devotees His column provides readers with succinct theoretical concepts and practical applications on topics as far reaching as digital modulation to antenna basics Difficult concepts are demystified as Robert shines a light on complex topics within electronic design This book collects sixteen Darker Side articles that have been

enriched with new exclusive content from the author An intro into The Darker Side will give examples of material that can enhance and optimize the way you design A Scilab tutorial along with Scilab software and all project material will be included with this package so that all projects can be tackled hands on It s time to stop being afraid of the dark let this book easily guide you through the time draining problematic elements of your application design Tips and tricks to enhance design performance Practical advice on topics from digital signal design to electromagnetic interference *ULSI Process*

Integration III Electrochemical Society. Meeting, 2003

Implantable Medical Electronics Vinod Kumar

Khanna, 2015-12-10 This book is a comprehensive interdisciplinary resource for the latest information on implantable medical devices and is intended for graduate students studying electrical engineering electronic instrumentation and biomedical engineering It is also appropriate for academic researchers professional engineers practicing doctors and paramedical staff Divided into two sections on Basic Concepts and Principles and Applications the first section provides an all embracing perspective of the electronics background necessary for this work The second section deals with pacing techniques used for the heart brain spinal cord and the network of nerves that interlink the brain and spinal cord with the major organs including ear and eye prostheses The four main offshoots of implantable electronics which this book discusses are The insertion of an implantable neural amplifier for accurate recording of neural signals for neuroengineering studies The use of implantable pulse generators for pacing the activities of diseased organs The use of implantable sensors for observing the influence of therapy and monitoring a patient s biological parameters The use of drug delivery systems to supervise the supply of accurate doses of medicine to affected parts Readers will also find chapters on the essentials of clocking and timing circuits pulse generator circuits neural amplifiers batteries biomaterials and biocompatibility and more Unique to this book is also a chapter on cyber security and confidentiality concerns with implants End of chapter questions and exercises help readers apply the content to practical use making this an ideal book for anyone wishing to learn more about implantable devices

Microwave Circuit Design Using Linear and Nonlinear Techniques George D. Vendelin, Anthony M. Pavio, Ulrich L.

Rohde, Matthias Rudolph, 2021-04-08 Four leaders in the field of microwave circuit design share their newest insights into the latest aspects of the technology The third edition of *Microwave Circuit Design Using Linear and Nonlinear Techniques* delivers an insightful and complete analysis of microwave circuit design from their intrinsic and circuit properties to circuit design techniques for maximizing performance in communication and radar systems This new edition retains what remains relevant from previous editions of this celebrated book and adds brand new content on CMOS technology GaN SiC frequency range and feedback power amplifiers in the millimeter range region The third edition contains over 200 pages of new material The distinguished engineers academics and authors emphasize the commercial applications in telecommunications and cover all aspects of transistor technology Software tools for design and microwave circuits are included as an accompaniment to the book In addition to information about small and large signal amplifier design and power amplifier

design readers will benefit from the book's treatment of a wide variety of topics like An in depth discussion of the foundations of RF and microwave systems including Maxwell's equations applications of the technology analog and digital requirements and elementary definitions A treatment of lumped and distributed elements including a discussion of the parasitic effects on lumped elements Descriptions of active devices including diodes microwave transistors heterojunction bipolar transistors and microwave FET Two port networks including S Parameters from SPICE analysis and the derivation of transducer power gain Perfect for microwave integrated circuit designers the third edition of Microwave Circuit Design Using Linear and Nonlinear Techniques also has a place on the bookshelves of electrical engineering researchers and graduate students It's comprehensive take on all aspects of transistors by world renowned experts in the field places this book at the vanguard of microwave circuit design research

Official Gazette of the United States Patent Office United States. Patent Office, 1964

Nanometer Frequency Synthesis Beyond the Phase-Locked Loop Liming Xiu, 2012-06-22 Introducing a new pioneering approach to integrated circuit design Nanometer Frequency Synthesis Beyond Phase Locked Loop introduces an innovative new way of looking at frequency that promises to open new frontiers in modern integrated circuit IC design While most books on frequency synthesis deal with the phase locked loop PLL this book focuses on the clock signal It revisits the concept of frequency solves longstanding problems in on chip clock generation and presents a new time based information processing approach for future chip design Beginning with the basics the book explains how clock signal is used in electronic applications and outlines the shortcomings of conventional frequency synthesis techniques for dealing with clock generation problems It introduces the breakthrough concept of Time Average Frequency presents the Flying Adder circuit architecture for the implementation of this approach and reveals a new circuit device the Digital to Frequency Converter DFC Lastly it builds upon these three key components to explain the use of time rather than level to represent information in signal processing Provocative inspiring and chock full of ideas for future innovations the book features A new way of thinking about the fundamental concept of clock frequency A new circuit architecture for frequency synthesis the Flying Adder direct period synthesis A new electronic component the Digital to Frequency Converter A new information processing approach time based vs level based Examples demonstrating the power of this technology to build better cheaper and faster systems Written with the intent of showing readers how to think outside the box Nanometer Frequency Synthesis Beyond the Phase Locked Loop is a must have resource for IC design engineers and researchers as well as anyone who would like to be at the forefront of modern circuit design

Electronics, 1945-07 June issues 1941 44 and Nov issue 1945 include a buyers guide section

[Electronic Experiences in a Virtual Lab](#) Roberto Gastaldi, Giovanni Campardo, 2020-05-11 This book presents a collection of lessons on various topics commonly encountered in electronic circuit design including some basic circuits and some complex electronic circuits which it uses as vehicles to explain the basic circuits they are composed of The circuits considered include a linear amplifier oscillators counters a digital clock power supplies a heartbeat detector a

sound equalizer an audio power amplifier and a radio The theoretical analysis has been deliberately kept to a minimum in order to dedicate more time to a learning by doing approach which after a brief review of the theory readers are encouraged to use directly with a simulator tool to examine the operation of circuits in a virtual laboratory Though the book is not a theory textbook readers should be familiar with the basic principles of electronic design and with spice like simulation tools To help with the latter aspect one chapter is dedicated to the basic functions and commands of the OrCad P spice simulator used for the experiments described in the book

Analog and Digital Circuits for Electronic Control System

Applications Gerald Luecke,2005 Today s control system designers face an ever increasing need for speed and accuracy in their system measurements and computations New design approaches using microcontrollers and DSP are emerging and designers must understand these new approaches the tools available and how best to apply them This practical text covers the latest techniques in microcontroller based control system design making use of the popular MSP430 microcontroller from Texas Instruments The book covers all the circuits of the system including Sensors and their output signals Design and application of signal conditioning circuits A to D and D to A circuit design Operation and application of the powerful and popular TI MSP430 microcontroller Data transmission circuits System power control circuitry Written by an experienced microcontroller engineer and textbook author the book is lavishly illustrated and includes numerous specific circuit design examples including a fully tested and documented hands on project using the MSP430 that makes use of the principles described For students engineers technicians and hobbyists this practical text provides the answers you need to design modern control systems quickly and easily Seasoned Texas Instruments designer provides a ground up perspective on embedded control systems Pedagogical style provides a self learning approach with examples quizzes and review features

Official Gazette of the United States Patent and Trademark Office United States. Patent and Trademark Office,2002 *RF CMOS Oscillators for Modern Wireless Applications* Masoud Babaie,Mina Shahmohammadi,Robert Bogdan Staszewski,2022-09-01 While mobile phones enjoy the largest production volume ever of any consumer electronics products the demands they place on radio frequency RF transceivers are particularly aggressive especially on integration with digital processors low area low power consumption while being robust against process voltage temperature variations Since mobile terminals inherently operate on batteries their power budget is severely constrained To keep up with the ever increasing data rate an ever decreasing power per bit is required to maintain the battery lifetime The RF oscillator is the second most power hungry block of a wireless radio after power amplifiers Consequently any power reduction in an RF oscillator will greatly benefit the overall power efficiency of the cellular transceiver Moreover the RF oscillators purity limits the transceiver performance The oscillator s phase noise results in power leakage into adjacent channels in a transmit mode and reciprocal mixing in a receive mode On the other hand the multi standard and multi band transceivers that are now trending demand wide tuning range oscillators However broadening the oscillator s tuning range is usually at the expense of

die area cost or phase noise The main goal of this book is to bring forth the exciting and innovative RF oscillator structures that demonstrate better phase noise performance lower cost and higher power efficiency than currently achievable Technical topics discussed in RF CMOS Oscillators for Modern Wireless Applications include Design and analysis of low phase noise class F oscillators Analyze a technique to reduce 1 f noise up conversion in the oscillators Design and analysis of low power low voltage oscillators Wide tuning range oscillators Reliability study of RF oscillators in nanoscale CMOS **Proceedings of the Thirtieth Annual Frequency Control Symposium, 1976** ,1976 **Comparators** Michael C. Fischer,2022-03-14 This book explores circuit designs that accomplish the conversion of an analog signal to a digital signal of a single bit Starting with the simple comparator many alternative circuit arrangements and enhancements are elaborated including hysteresis negative feedback and a variety of adaptive thresholds Further the non ideal behavior of practical elements and circuits are covered including input offsets noise delay delay dispersion and oscillation along with techniques for dealing with these aspects The wide variety of available components is discussed in terms of performance and applicability No stone is left unturned in addressing each and every issue that can affect the engineering tasks related to comparators from the viewpoint of how their performance can affect the system in which they are a critical component **MSP430 Microcontroller Lab Manual** James Kretzschmar,Jeffrey Anderson,Steven F. Barrett,2023-05-23 This book is a practical reference for using Texas Instruments MSP430 microcontrollers It provides a series of hands on laboratory exercises The labs may be completed in a traditional laboratory setting or at home using the Digilent Analog Discovery 2 Test Instrument This book can be used as a reference for planning future projects using the MSP430 microcontroller The authors focus on applications of the main peripheral modules available on the MSP430 microcontroller CPU clock Basic Input Output Timer Analog to Digital Converter They also provide examples of how to develop Pulse Width Modulation signals and how to use Interrupts
Proceedings of the Annual Symposium on Frequency Control ,1975

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